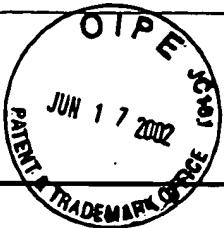


Substitute for Form 1449A/P-10 (Modified) (use as many sheets as necessary)		Attorney Docket No.: 4640.P006	Application Number: 10/083,241
Page 1 of 2		First Named Inventor: Yervant Zorian	RECEIVED JUN 24 2002
		Filing Date: February 25, 2002	Technology Center 2100

**U.S. PATENT DOCUMENTS**



Exam. Initial*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Patent of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (If known)			
DT		5,513,318		van de Goor et al.	4-30-1996	
DT		5,570,374		Yau et al.	10-29-1996	
DT		5,960,009		Gizopoulos et al.	9-28-1999	
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DO		6,085,334		Giles et al.	7-4-2000	
DT		6,205,564	B1	Kim et al.	3-20-2001	
DT		6,237,123	B1	Kim et al.	5-22-2001	
DT		6,317,846	B1	Higgins et al.	11-13-2001	
DT		6,330,696	B1	Zorian et al.	12-11-2001	
DT		6,397,349	B2	Higgins et al.	5-28-2002	

FOREIGN PATENT DOCUMENTS						
Exam. Initial*	Cite No. <sup>1</sup>	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>3</sup> (If known)		

Examiner Signature	<i>Davoodian</i>	Date Considered <i>12/16/04</i>
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<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). <sup>4</sup>For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. <sup>3</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>4</sup>Applicant is to place a check mark here if English language Translation is attached.

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Substituted Form 1449/PTO		<i>Complete if Known</i>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>		Application Number	10/083,241
		Filing Date	February 25, 2002
		First Named Inventor:	Yervant Zorian
		Art Unit	2133
		Examiner Name	Unassigned
Sheet	TRADEMARKS	of	2
		Attorney Docket Number	
		04640.P006	

## **U.S. PATENT DOCUMENTS**

## **FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>2</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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<i>Complete If Known</i>	
Application Number	10/083,241
Filing Date	February 25, 2002
First Named Inventor:	Yervant Zorian
Art Unit	2133
Examiner Name	Unassigned
Attorney Docket Number	04640.P006

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
DT		Yervant Zorian: "System-Chip Test Strategies" (Tutorial): DAC 1998: 752-757.	
DT		Ilyoung Kim, Yervant Zorian, Goh Komoriya, Hai Pham, Frank P. Higgins, Jim L. Lewandowski: " Built-in Self-Repair for Embedded High Density SRAM." ITC 1998: 1112-1119.	
DT		Yervant Zorian, Erik Jan Marinissen, Sujit Dey: "Testing Embedded-Core Based System Chips." ITC 1998: pages 1-14.	
DT		Yervant Zorian, Erik Jan Marinissen, "System Chip Test: How Will It Impact Your Design?" pages 1-6, DAC, 2000.	
DI		Monica Lobetti Bodoni, Alfredo Benso, Silvia Chiusano, Stefano DiCarlo, Giorgio DiNatale, Paolo Prinetto, "An Effective Distributed BIST Architecture for RAMs," pages 1-6, IEEE European Test Workshop, 2000.	
DT		Chauchin Su, Shih-Ching Hsiao, Hau-Zen Zhai, Chung-Len Lee, "A Computer Aided Engineering System for Memory BIST," pages 1-4, DAC, 2001.	

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